

10/730,123

(sjp)

SRNT (East) 12/6/05

Type	L#	Hits	Search Text	DBs	Time Stamp	Comments
IS&R	L5	239	((327/26) or (327/37)).CCLS.	US-PGPUB; USPAT	12/6/05 15:11	See Below for UPDATE
IS&R	L6	529	(327/31).CCLS.	US-PGPUB; USPAT	12/6/05 15:11	See Below for UPDATE
IS&R	L7	303	(374/170).CCLS.	US-PGPUB; USPAT	12/6/05 15:11	See Below for UPDATE
BRS	L8	4	5 and @pd>"20050709"	US-PGPUB; USPAT	12/6/05 15:12	b = browsed
BRS	L9	3	6 not 5 and @pd>"20050709"	US-PGPUB; USPAT	12/6/05 15:15	b
BRS	L10	6	7 not 6 not 5 and @pd>"20050709"	US-PGPUB; USPAT	12/6/05 15:21	b
BRS	L11	1	( temperature with detec\$3 ) and 5	US-PGPUB; USPAT	12/6/05 15:31	b
BRS	L12	7	( temperature with detec\$3 ) and 6 not 5	US-PGPUB; USPAT	12/6/05 15:25	b
BRS	L13	3	( temperature with detect\$3 ) and (5 or 6) not 11 not 12	US-PGPUB; USPAT	12/6/05 15:32	b
BRS	L14	125	(trigger\$3 adj potential) and temperature	US-PGPUB; USPAT	12/6/05 15:36	see below
BRS	L15	88	(trigger\$3 adj potential) and temperature and puls\$3	US-PGPUB; USPAT	12/6/05 15:37	see below
BRS	L16	36	(trigger\$3 adj potential) and temperature and (puls\$3 near2 width)	US-PGPUB; USPAT	12/6/05 15:37	b
BRS	L18	22	( (RC adj delay) with (inverter or NAND) ) and Schmitt	US-PGPUB; USPAT	12/6/05 15:43	b
BRS	L19	2560	( temperature and (inverter or NAND or logic) ) and Schmitt	US-PGPUB; USPAT	12/6/05 16:08	see below
BRS	L20	337	( temperature and (inverter or NAND or logic) ) and Schmitt and integrator	US-PGPUB; USPAT	12/6/05 16:09	see below
BRS	L21	115	( temperature and (inverter or NAND or logic) ) and Schmitt and integrator and latch	US-PGPUB; USPAT	12/6/05 16:09	see below
BRS	L22	67	( temperature and (inverter or NAND or logic) ) and Schmitt and integrator and latch and (pulse near width)	US-PGPUB; USPAT	12/6/05 16:10	b
BRS	L23	0	(( temperature and (inverter or NAND or logic) ) and Schmitt and integrator and latch).clm.	US-PGPUB; USPAT	12/6/05 16:14	
BRS	L24	1	(( temperature and (inverter or NAND or logic) ) and Schmitt and integrator).clm.	US-PGPUB; USPAT	12/6/05 16:15	b
BRS	L25	38	(( temperature and (inverter or NAND or logic or Exor ) ) and Schmitt).clm.	US-PGPUB; USPAT	12/6/05 16:18	b
IS&R	L26	1282	((361/103) or (361/93.8) or (361/106)).CCLS.	US-PGPUB; USPAT	12/6/05 16:56	see next
BRS	L27	11	26 and 19	US-PGPUB; USPAT	12/6/05 16:57	b